

Jordi Pakey-Rodriguez

Hardware Engineer

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San Francisco, CA

503.317.9833

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Skills

High-speed digital design/layout (PCIe 2.0, DDR3, SGMII), advanced board layout (HDI, rigid-flex, high power), embedded hardware bringup (C/C++ firmware, Verilog HDL, embedded python)

Software: Altium, KiCad, SolidWorks, FreeCAD, Git

Experience

Zeroform Design / Founder & Engineer

JANUARY 2021 - PRESENT

Registered Zeroform LLC in the state of Oregon as an umbrella for future hardware projects. Currently working on our first commercial product, SAM-ISM, a tiny embeddable packet radio transceiver and controller.

Independent Consulting / Hardware Design Engineer

JULY 2020 - PRESENT

Developed or assisted with several open hardware products for 1BitSquared, most of which involved embedded FPGAs. Also started learning Verilog and other FPGA design skills.

Uber ATG / Autonomy Hardware Engineer I & II

JUNE 2018 - MAY 2020, SAN FRANCISCO

Initially worked on the Otto team designing power electronics and drive-by-wire hardware for autonomous long-haul trucks. Later moved to the LiDAR team and did high-speed analog and digital board design, including HDI and rigid-flex, for our in-house automotive LiDAR.

Education

University of Illinois / B.S. Electrical Engineering

AUGUST 2013 - MAY 2018, URBANA-CHAMPAIGN

Drum Major, Marching Illini: served as one of three Drum Majors for the 2016 and 2017 seasons. Drum Majors are the student leaders of the band. They go through an intense audition process and are responsible for teaching, conducting, and behind-the-scenes planning and execution.

Technical Lead, STELLARs: a NSF-funded grant initiative to develop a novel K-12 learning system meant to simulate NASA planetary exploration. Build a small Mars rover to be used as a teaching tool.

Coursework: Semiconductor Devices, Digital Systems Lab, Intro to Robotics, Data Structures & Algorithms